

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 254611US90DIV		SERIAL NO. 10/780,856					
<div style="border: 1px solid black; border-radius: 50%; padding: 10px; display: inline-block;"> PIPE AUG 25 2004 PATENT & TRADEMARK OFFICE </div>				APPLICANT Yasushi INAGAKI, et al.							
LIST OF REFERENCES CITED BY APPLICANT				FILING DATE February 19, 2004		GROUP 2841					
U.S. PATENT DOCUMENTS											
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE				
JD 	AA	5,661,882	09/02/1997	J. H. ALEXANDER	I 	I 	I 				
	AB	5,018,051	05/21/1991	H. YAMADA, et al.							
	AC	5,877,550	03/02/1999	K. SUZUKI							
	AD	4,800,459	01/24/1989	H. TAKAGI, et al.							
	AE	5,432,677	07/11/1995	L. J. MOWATT, et al.							
	AF										
	AG										
	AH										
	AI										
	AJ										
	AK										
	AL										
	AM										
	AN										
	FOREIGN PATENT DOCUMENTS										
			DOCUMENT NUMBER	DATE				COUNTRY	TRANSLATION		
					YES	NO					
JD JD	AO	0 451 541'	10/16/1991	EUROPE	—	X					
	AP	0 929 207	07/14/1999	EUROPE	—	—					
	AQ										
	AR										
	AS										
	AT										
	AU										
	AV										
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)											
	AW	<div style="border: 1px solid black; height: 100px; width: 100%;"></div>									
	AX										
	AY										
	AZ							<input type="checkbox"/> Additional References sheet(s) attached			
Examiner Tianhui					Date Considered 7-28-05						
<small>*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>											

Atty. Dkt. No.	M#	Client Ref.
	307277	7048P-U1

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**

Applicant: INAGAKI, Yasushi et al.

Divisional of Appln. No.: 09/830,360 filed 4/25/01

Filing Date: February 18, 2004

Date: February 19, 2004

Page 1 of 1

Examiner: TBA Group Art Unit: TBA 2841

U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
TD	AR 5,027,253	06/1991	Lauffer et al.	I	I	I
	BR 5,559,363	09/1996	Immorlica, Jr., Anthony A.	I	I	I
	CR 5,565,706	10/1996	Miura et al.	I	I	I
	DR 5,745,984	05/1998	Cole et al.	I	I	I
	ER 5,875,100	02/1999	Yamashita, Koji	I	I	I
TD	FR 6,021,050	02/2000	Ehman et al.	I	I	I
	GR 6,153,290	11/2000	Sunahara, Hirofumi	I	I	I
	HR					
	IR					

FOREIGN PATENT DOCUMENTS

	Document Number	Date MM/YYYY	Country	Inventor Name	English Abstract		Translation Readily Available	
					Enclosed	No	Enclose	No
TD	JR 11-248311	09/1999	Japan	Sakano et al.	I	I	I	I
	KR 6-326472	11/1994	Japan	Furusawa	I	I	I	I
	LR 7-263619	10/1995	Japan	Ito	I	I	I	I
	MR 10-256429	09/1998	Japan	Iyogi et al.	I	I	I	I
	NR 11-45955	06/1989	Japan	Makiura	I	I	I	I
TD	OR 11-126978	05/1999	Japan	Fujisaki	I	I	I	I
	PR 11-312868	11/1999	Japan	Hayashi	I	I	I	I
	QR							
	RR							
	SR							

OTHER (including in this order: Author, Title, Periodical Name, Date, Pertinent Pages, etc.):

TD	TR	PATENT ABSTRACTS OF JAPAN, "MULTILAYERED WIRING BOARD" JP 11-126978, May 11, 1999, Akiya, Fujisaki et al			
	UR	PATENT ABSTRACTS OF JAPAN "METHOD FOR MANUFACTURING MULTILAYER PRINTED WIRING BOARD", JP 10-013024, January 1, 1998, Satoshi Nakamura			
	VR	PATENT ABSTRACTS OF JAPAN, "ELECTRODE STRUCTURE AND THE MANUFACTURING METHOD", JP 54-157296, December 12, 1979			
TD	WR	PATENT ABSTRACTS OF JAPAN, "MULTILAYER ELECTRONIC PART AND MANUFACTURE THEREOF", JP 8-241827, September 17, 1996			
	XR				

Examiner

Date Considered: 7-28-03

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Inventor(s): INAGAKI et al.
Atty. Dkt: 041226/0307277
Appln. No.: NEW APPLICATION
Filed: February 19, 2004
FOR: PRINTED CIRCUIT BOARD AND METHOD OF MANUFACTURING PRINTED CIRCUIT BOARD

Group Art Unit: TBA
Examiner: TBA

Date: February 19, 2004

IDS LETTER CITING APPLICATION(S)

The Examiner's attention is directed to the following application(s) of:

Examiner's Initials*	FIRST INVENTOR (Last Name Only)	APPLICATION NO. & Filing Date	ENCLOSED
TD	INAGAKI	09/830,361 April 25, 2001	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other..

PLEASE DO NOT PRINT the above information on the patent which results from the subject application.

Consideration of the above listed application is earnestly solicited since unpublished patent applications are contemplated as IDS material; see the exception in Rule 98(a)(2)(iii) and note the penultimate sentence of MPEP 609.

Further, in keeping with MPEP 609, Subsec. C(2), 2nd para., line 10 to end of the paragraph (especially note lines 18-25) **PLEASE RETURN A COPY OF THIS LETTER** with the Examiner's initials adjacent each above listing so that applicant will know that the application listed above has been considered as required by PTO policy.

Respectfully submitted

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*The Examiner's initials adjacent a citation indicates he/she has considered the cited application relative to the subject application.